

IN THE TITLE

Please amend the title as follows:

POWER-ON RESET CIRCUIT FOR ERASING METHOD-OF SPLIT GATE FLASH  
MEMORY REFERENCE CELLS

IN THE SPECIFICATION

Please add this paragraph before the first paragraph on page 1:

This is a continuation application of U.S. Patent Application 10/197,049 filed on 07/17/2002,  
*Patent No. 6,711,062*  
now allowed.